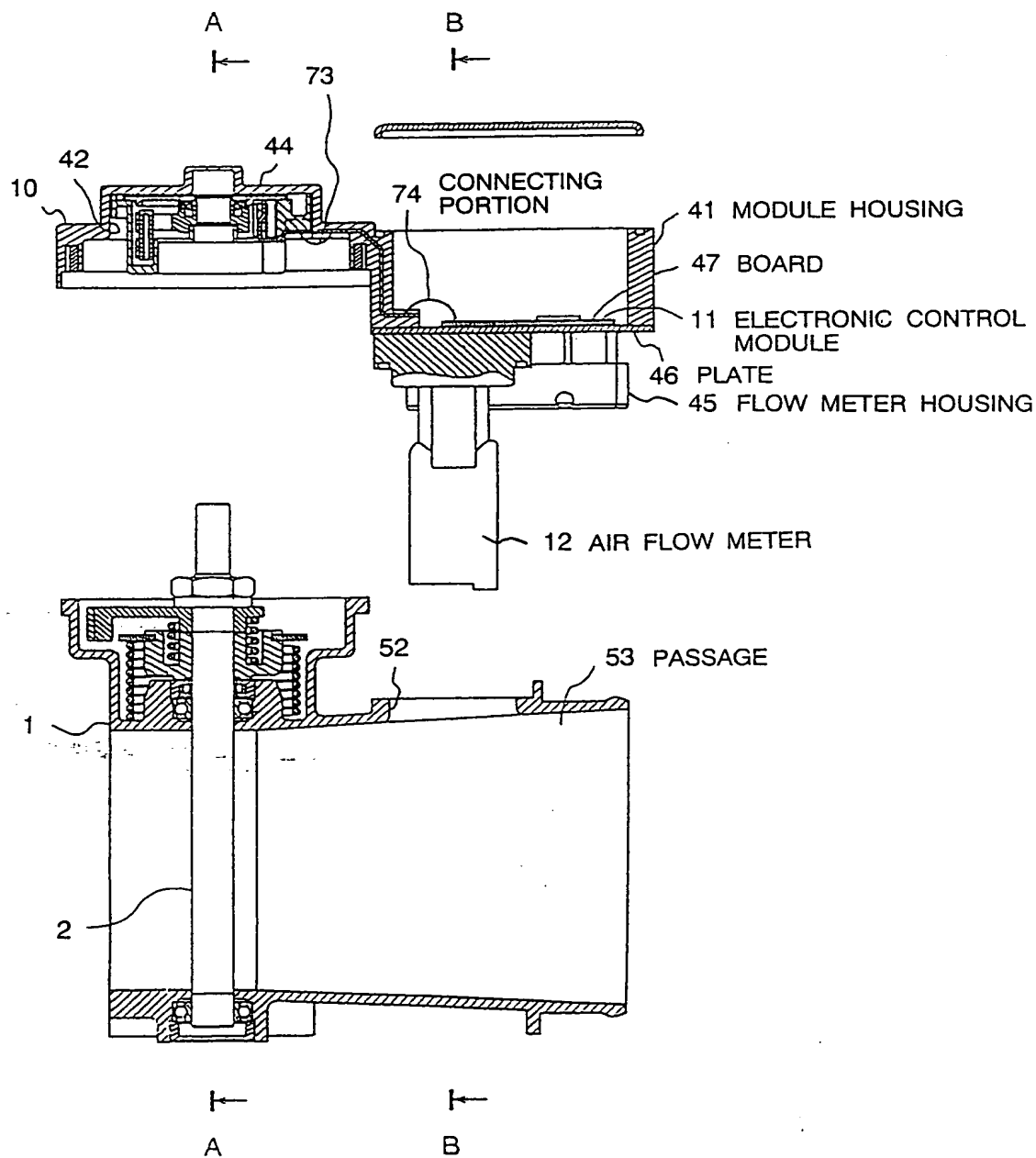
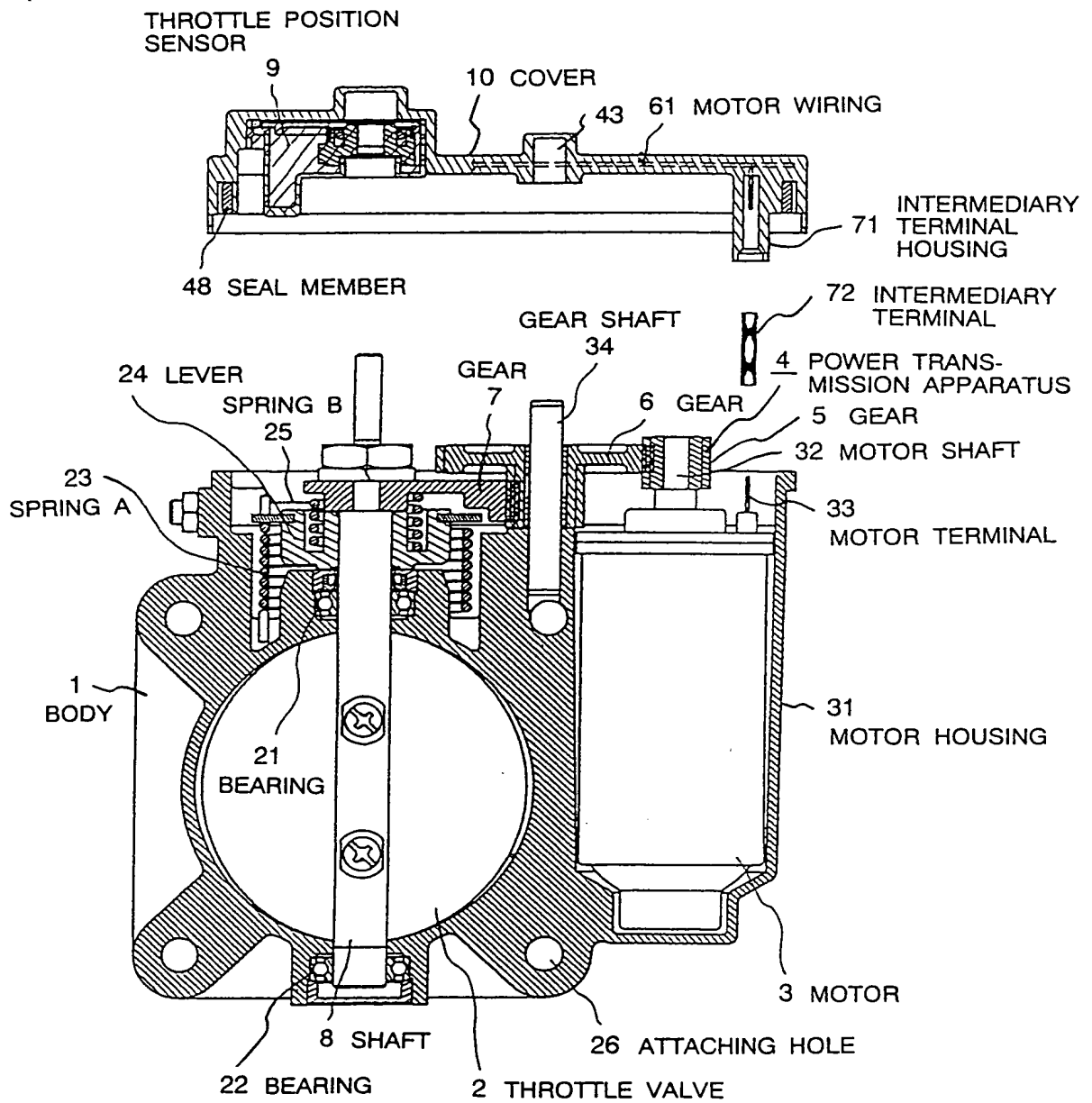


1/22

**FIG. 1**

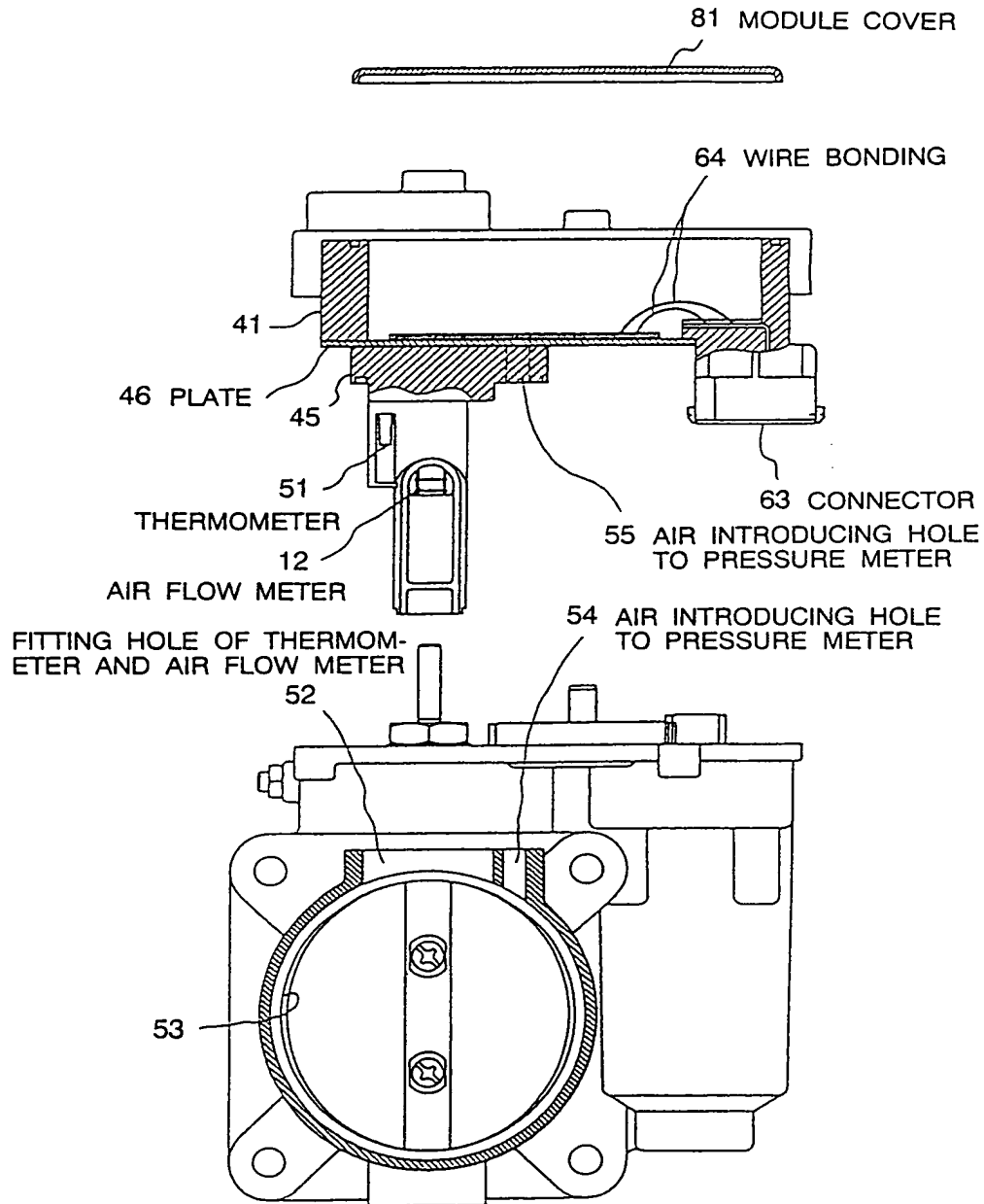


**FIG. 2**



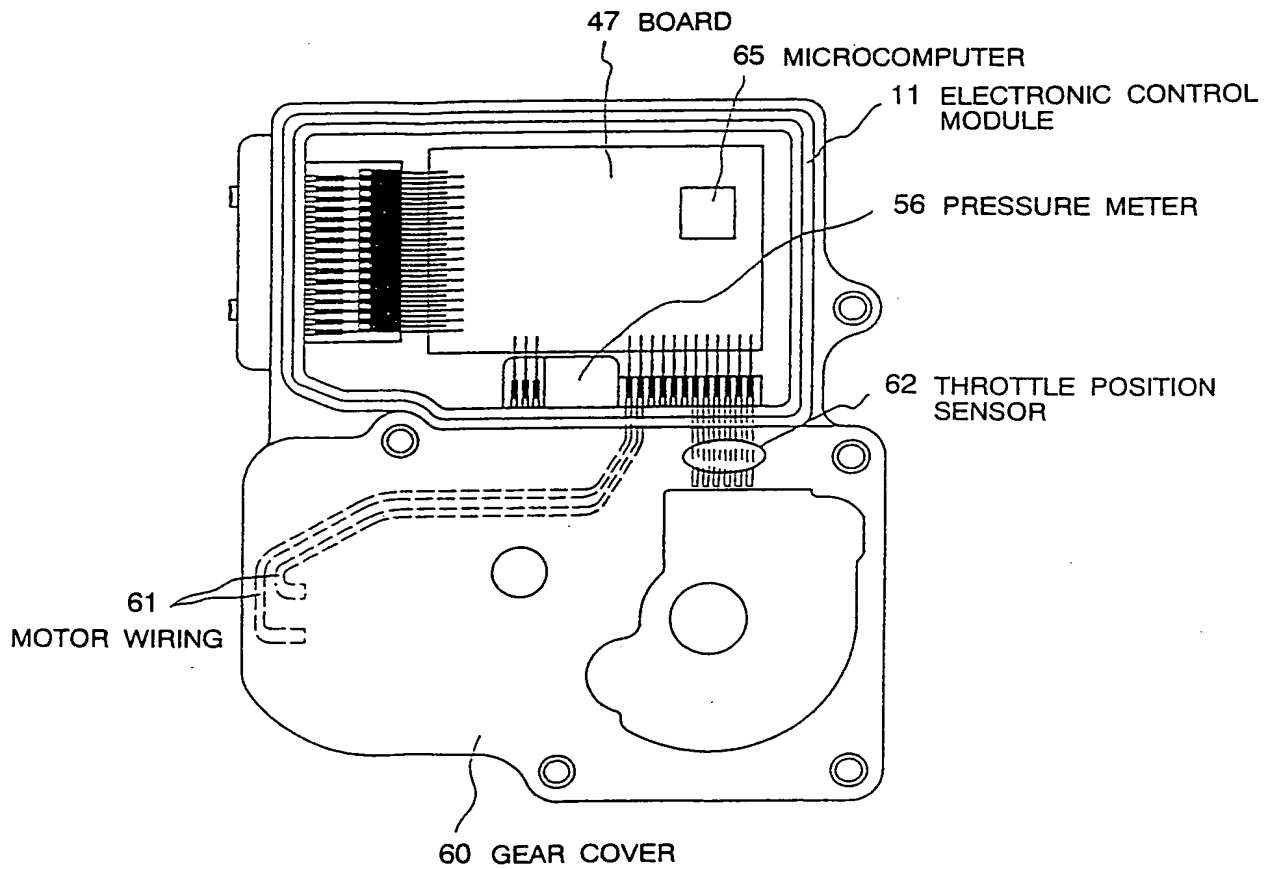
3/22

**FIG. 3**



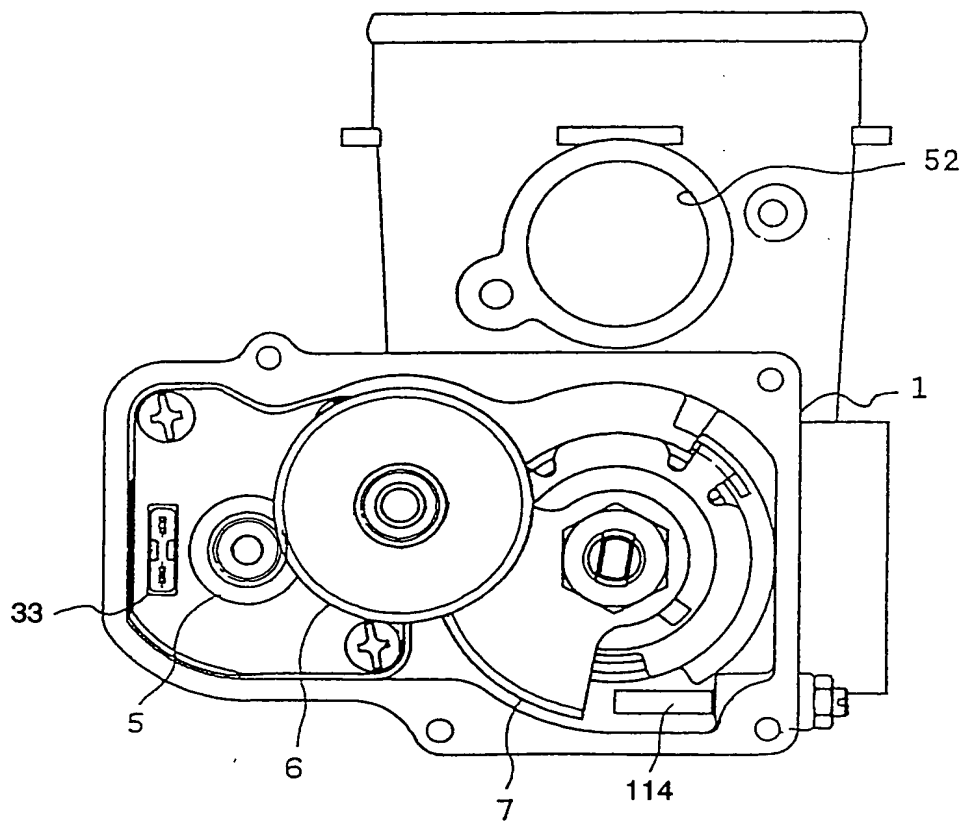
4/22

**FIG. 4**



5/22

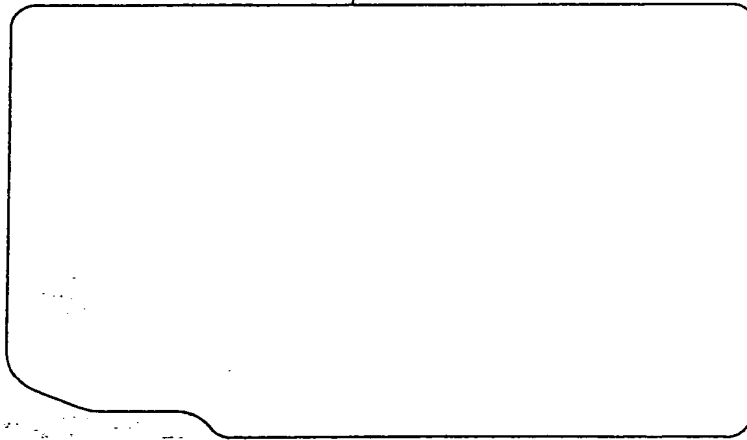
**FIG. 5**

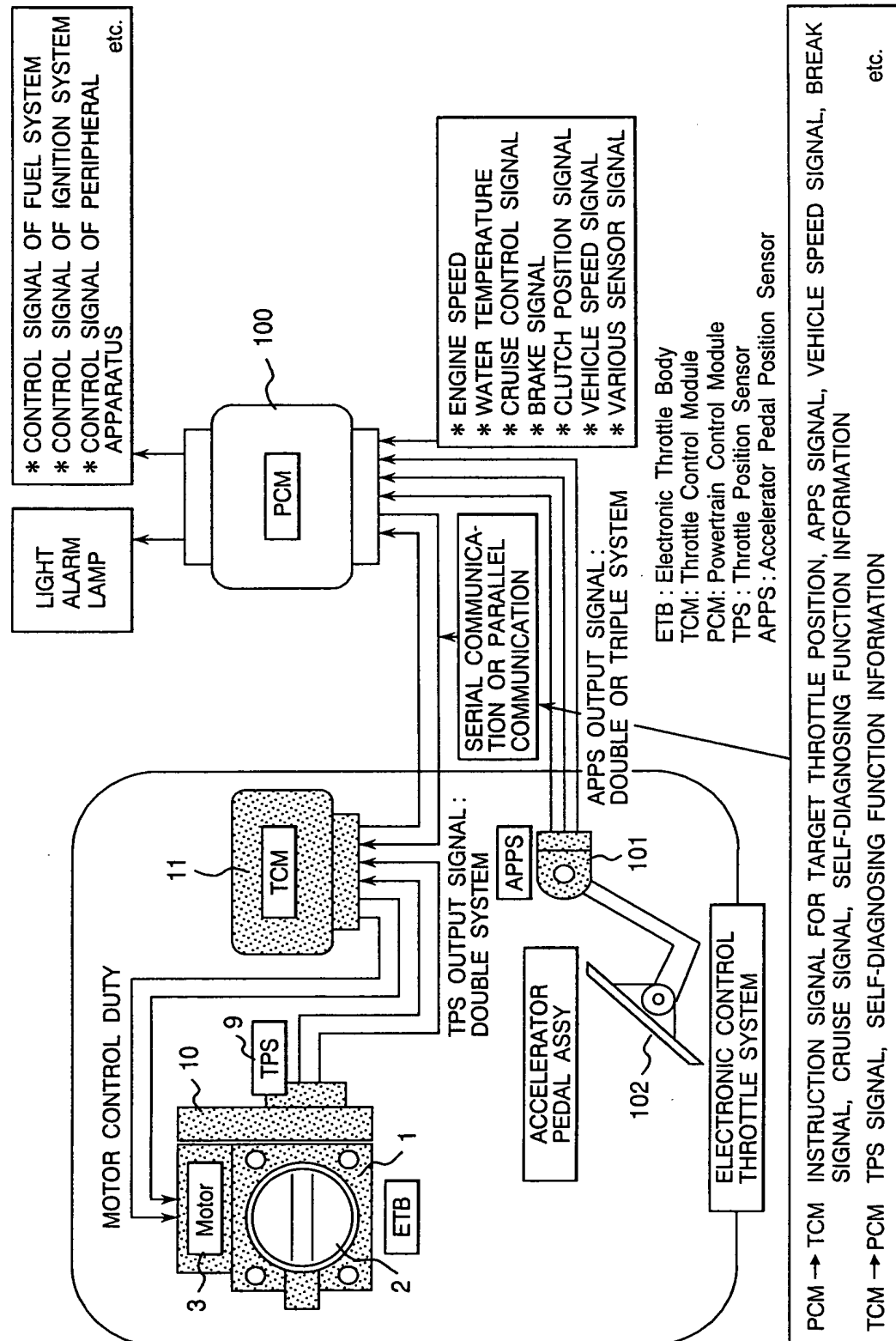


6/22

***FIG. 6***

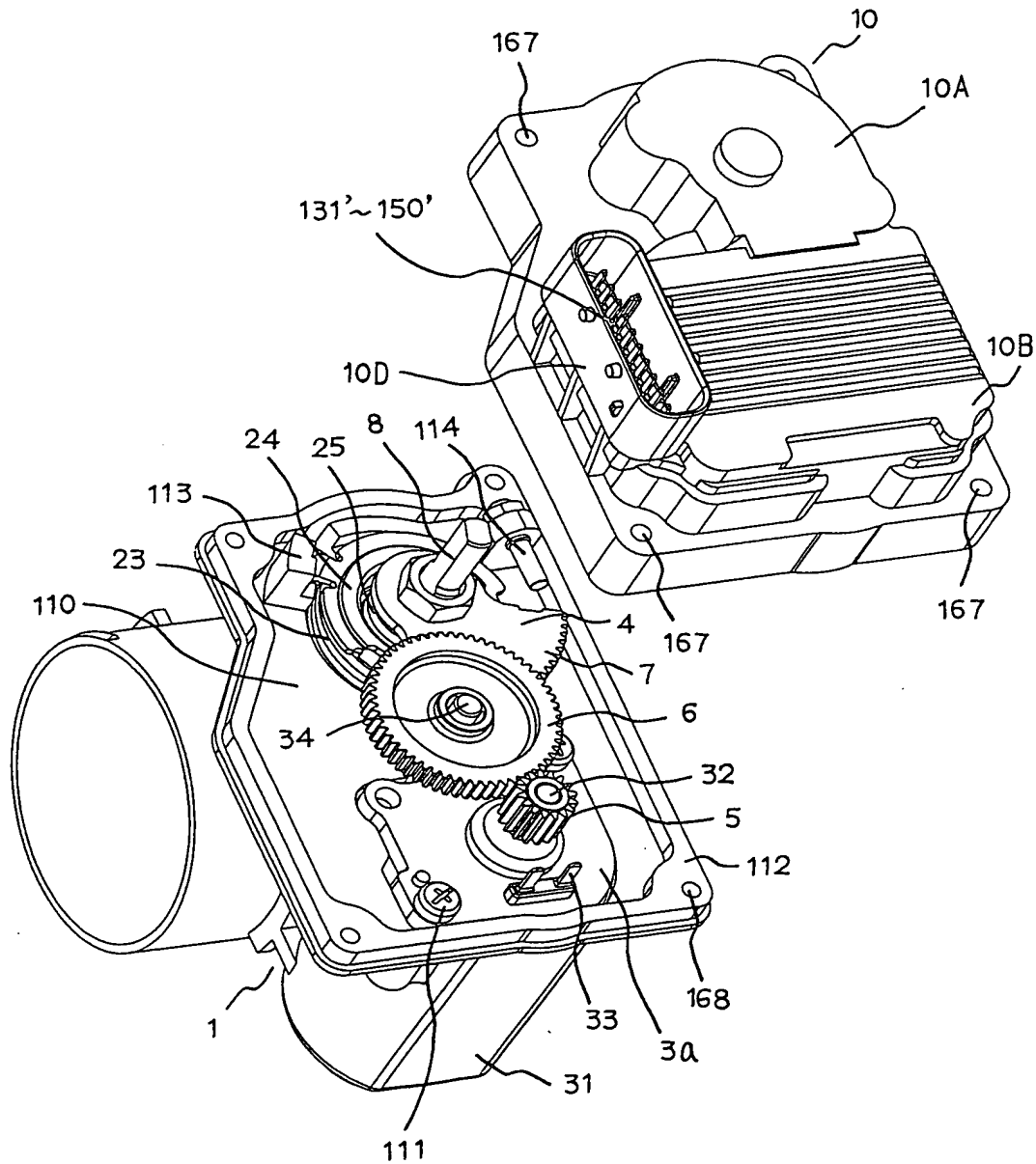
81 MODULE COVER





8/22

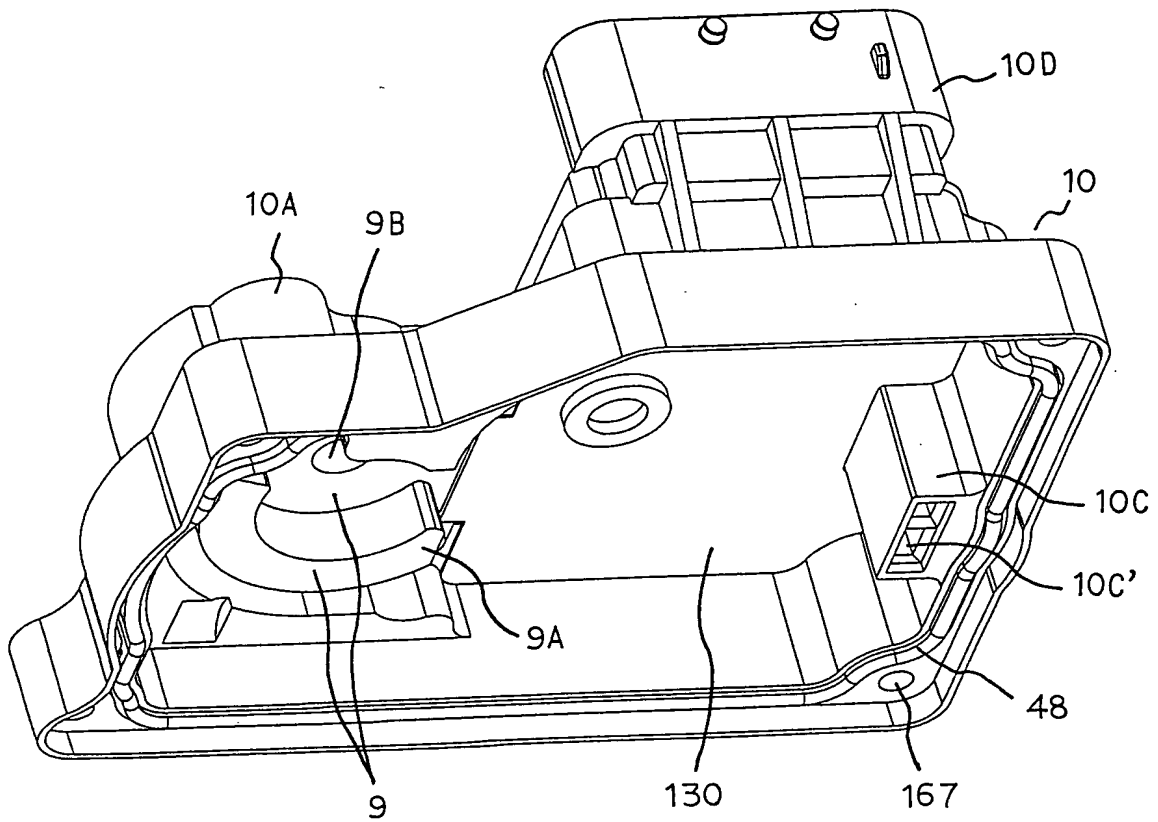
FIG. 8





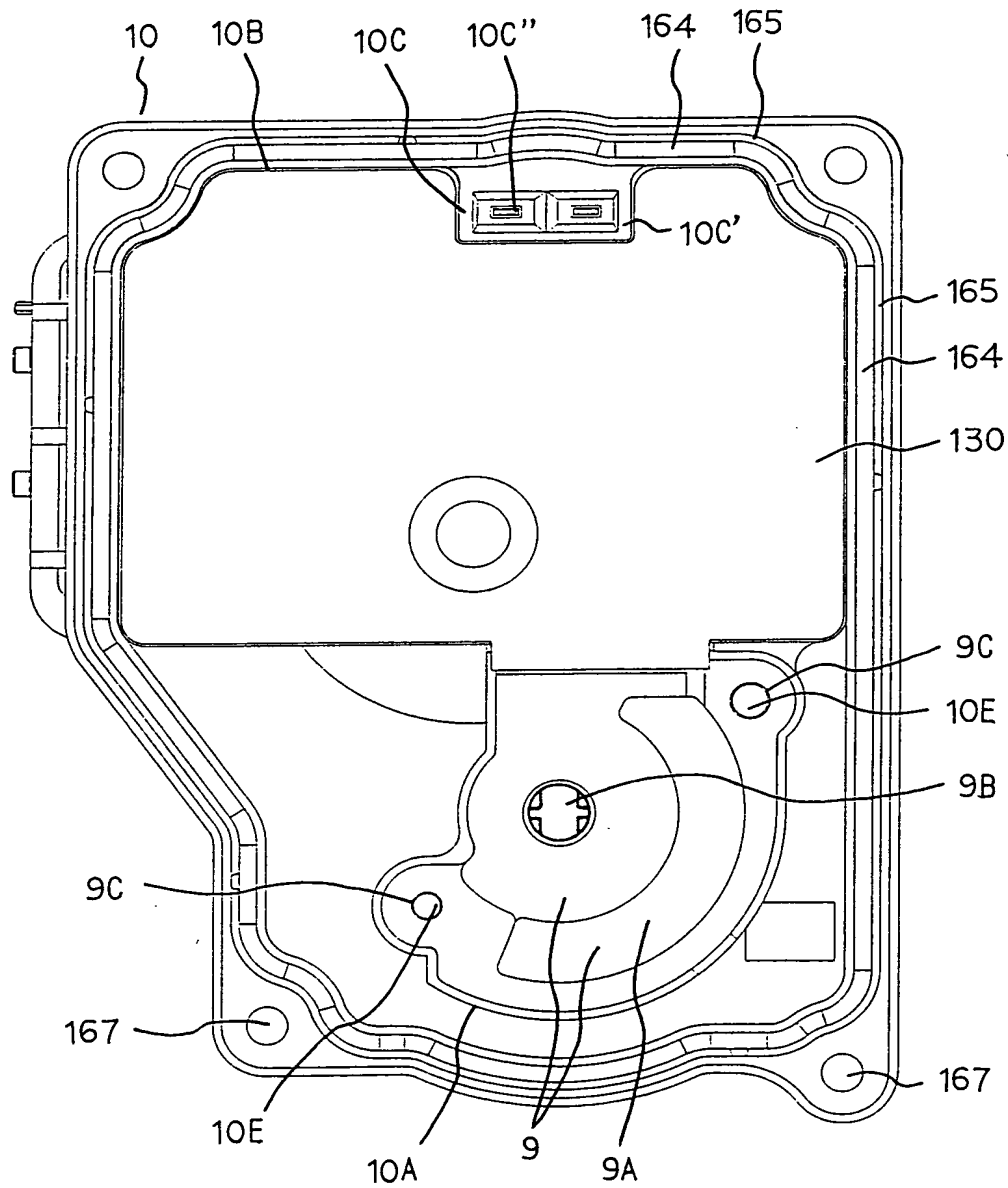
9/22

**FIG. 9**



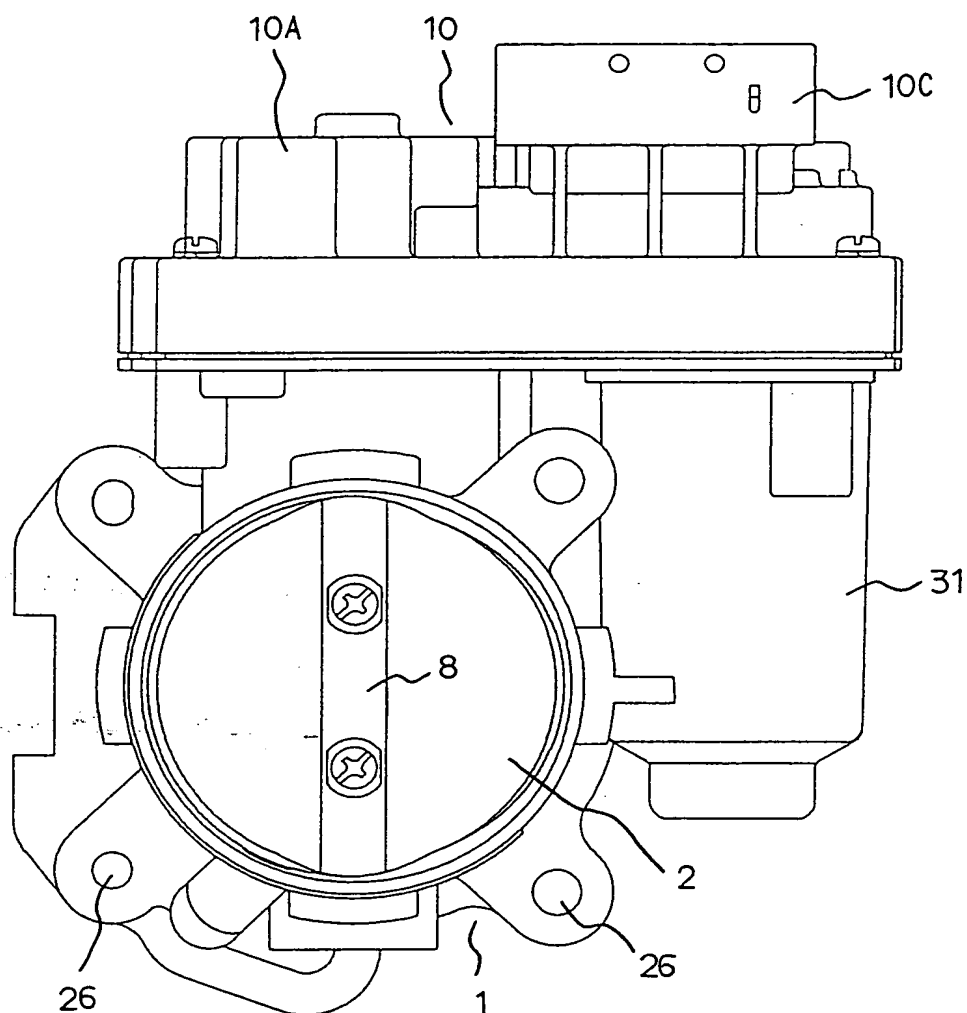
10/22

**FIG. 10**



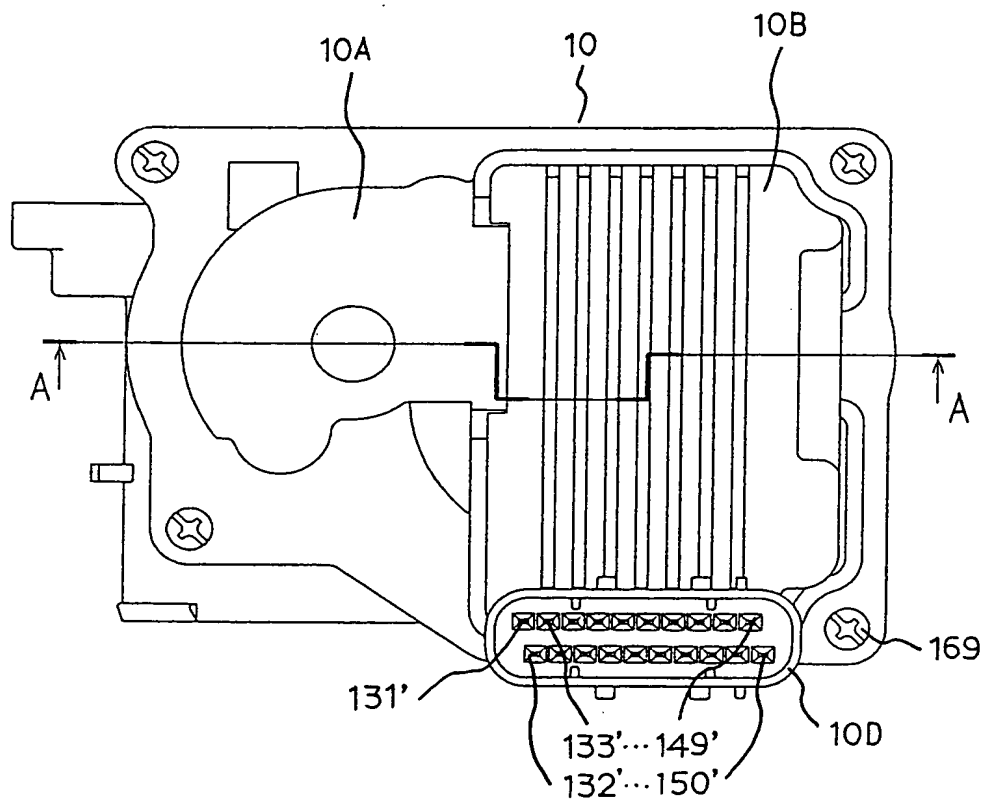
11/22

**FIG. 11**



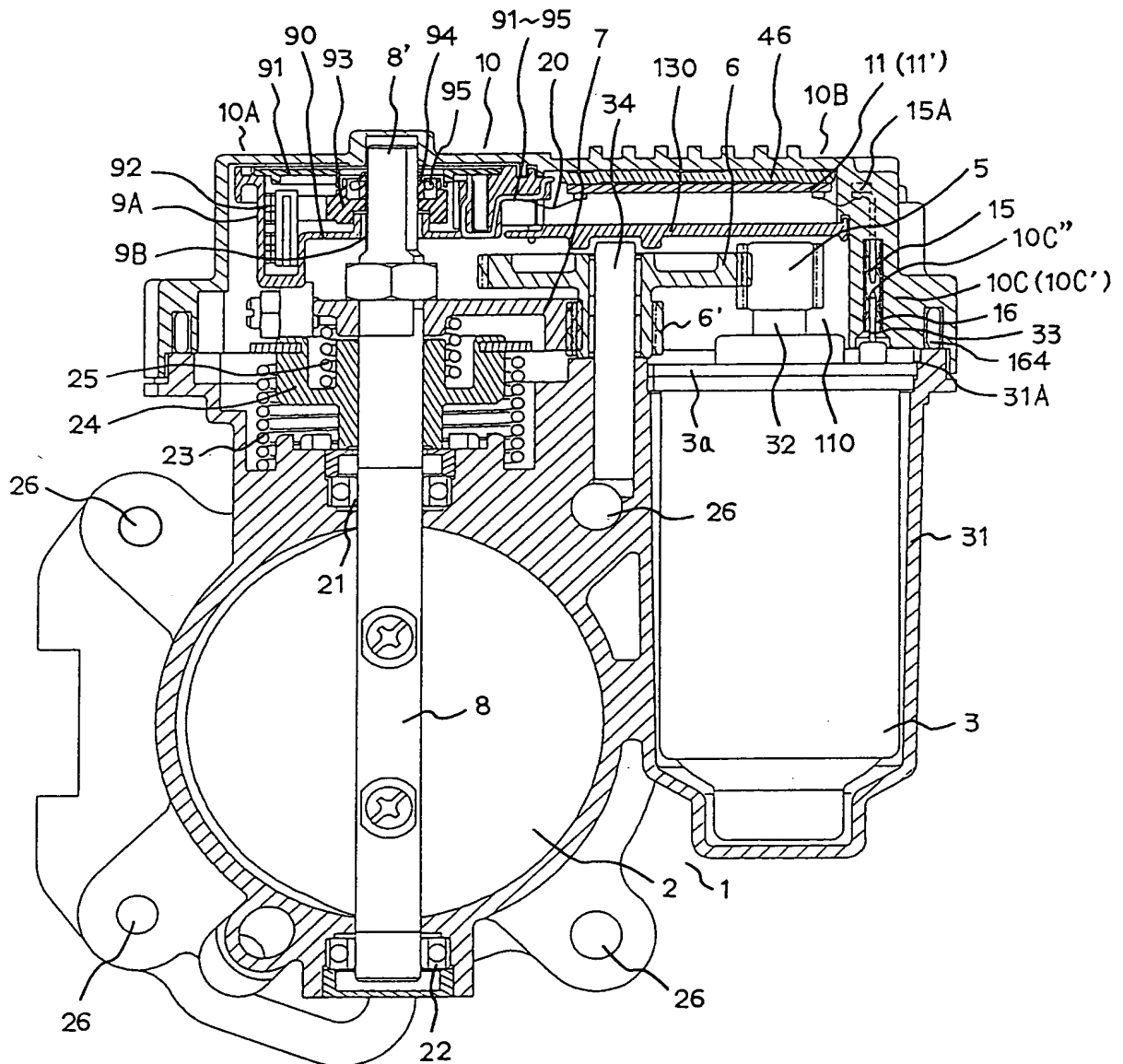
12/22

**FIG. 12**



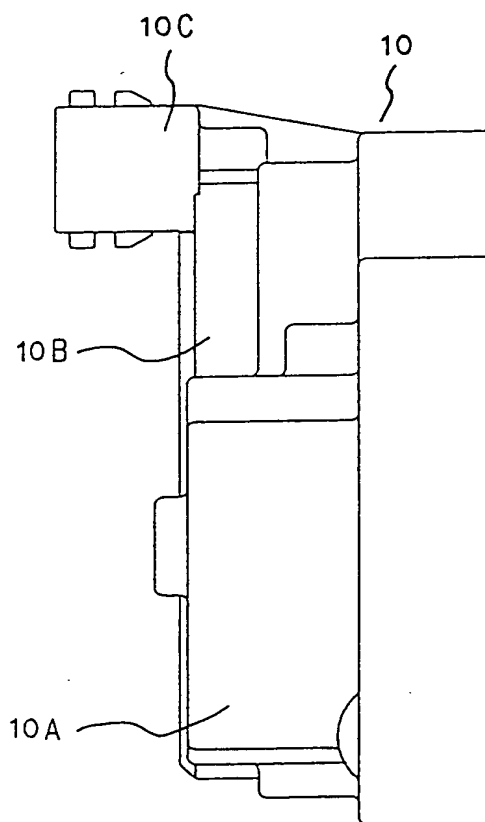
13/22

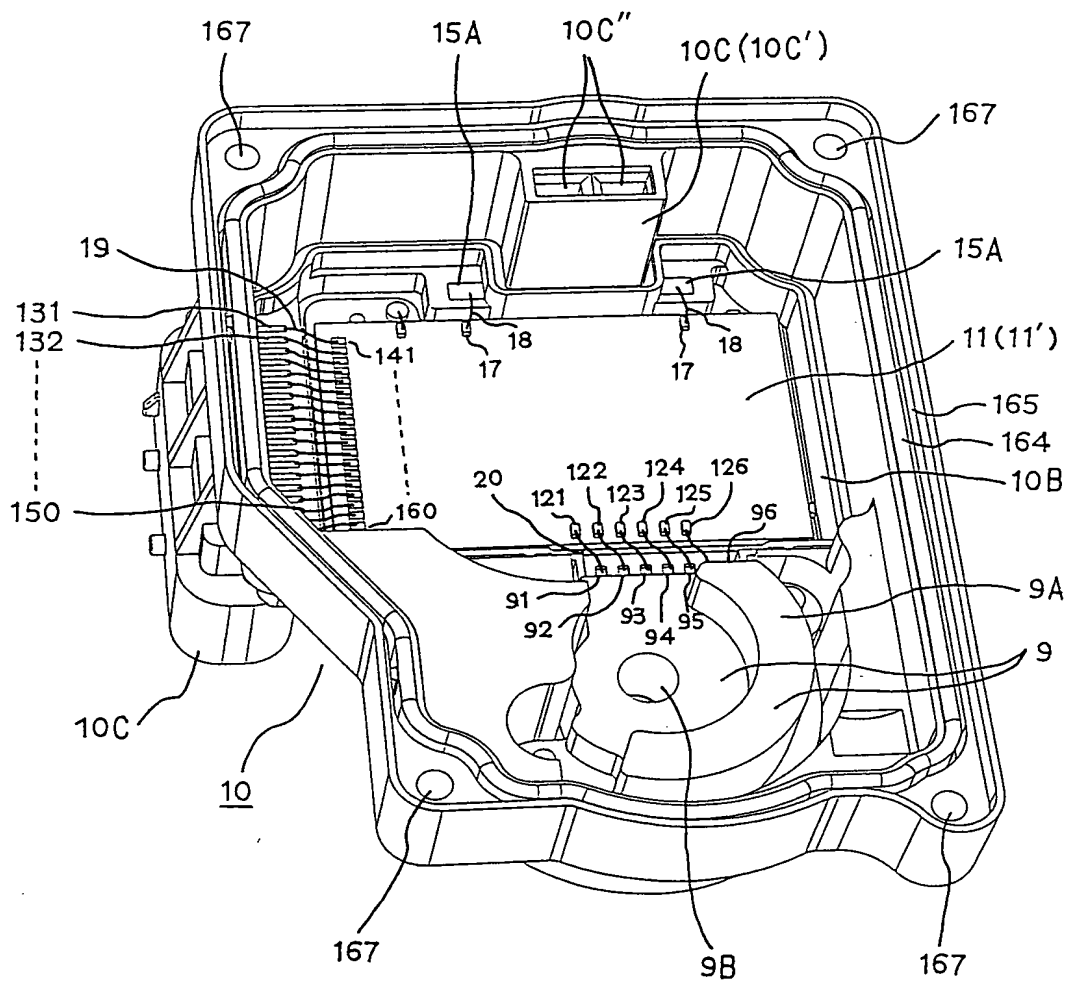
FIG. 13

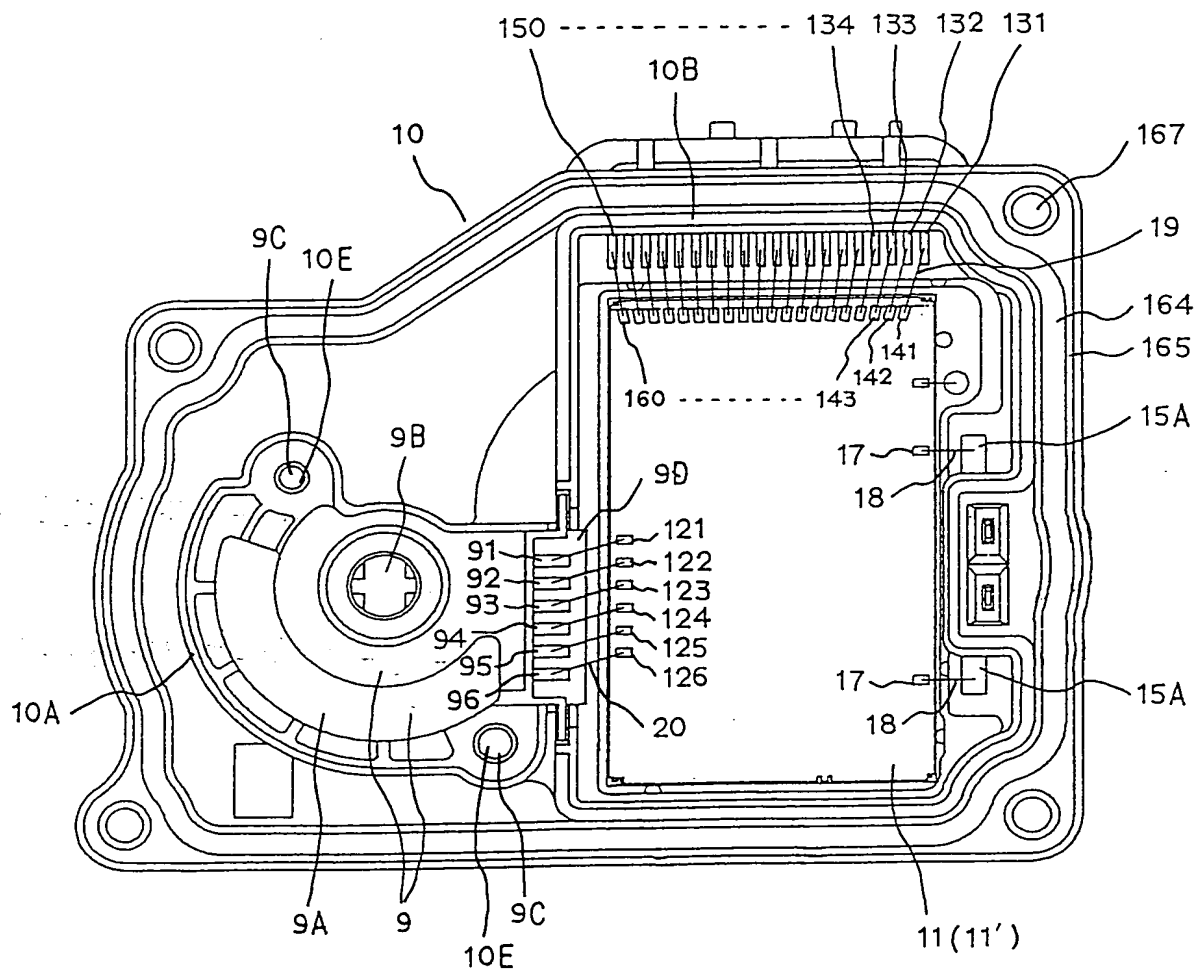


14/22

**FIG. 14**



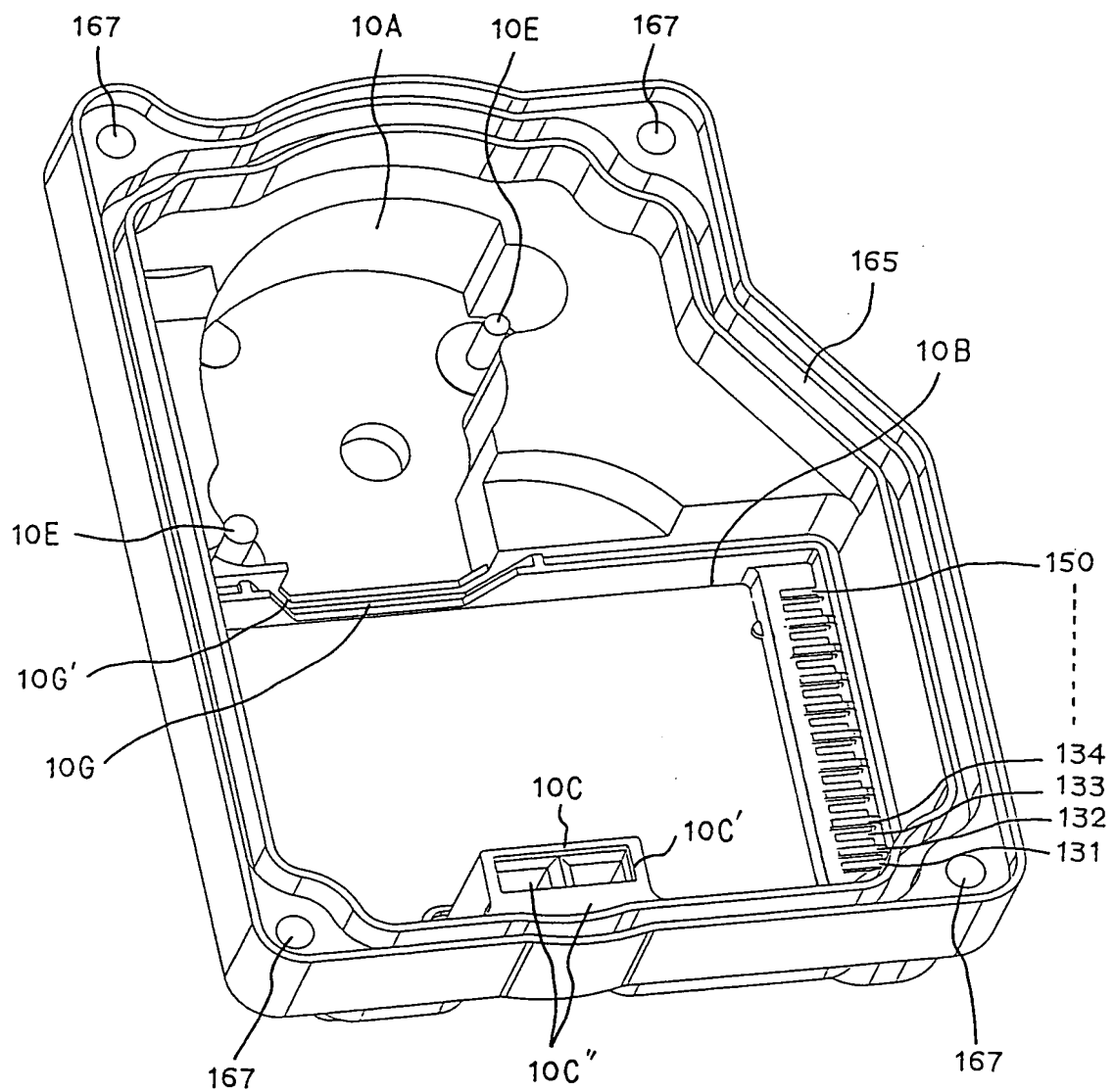






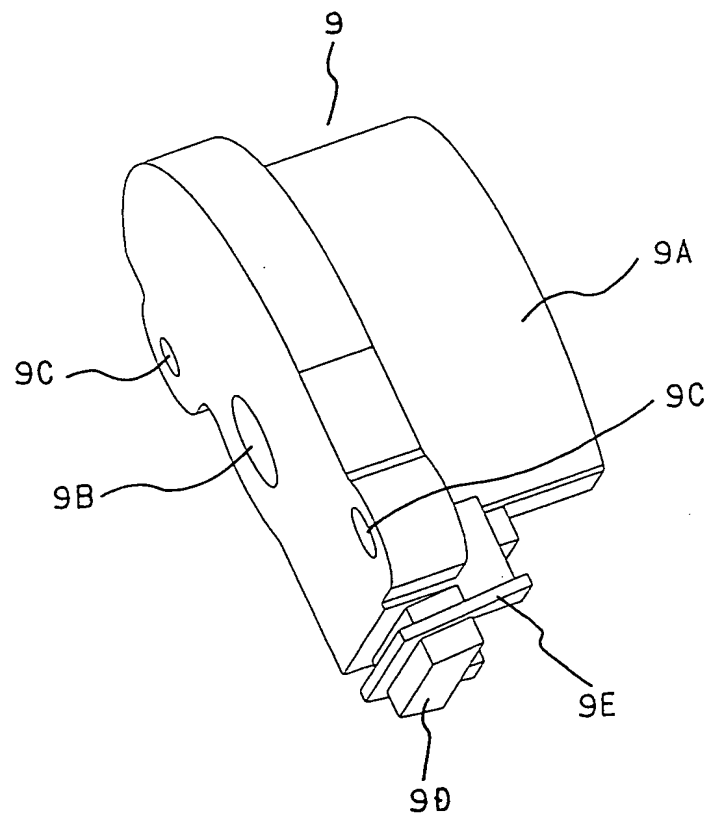
17/22

**FIG. 17**



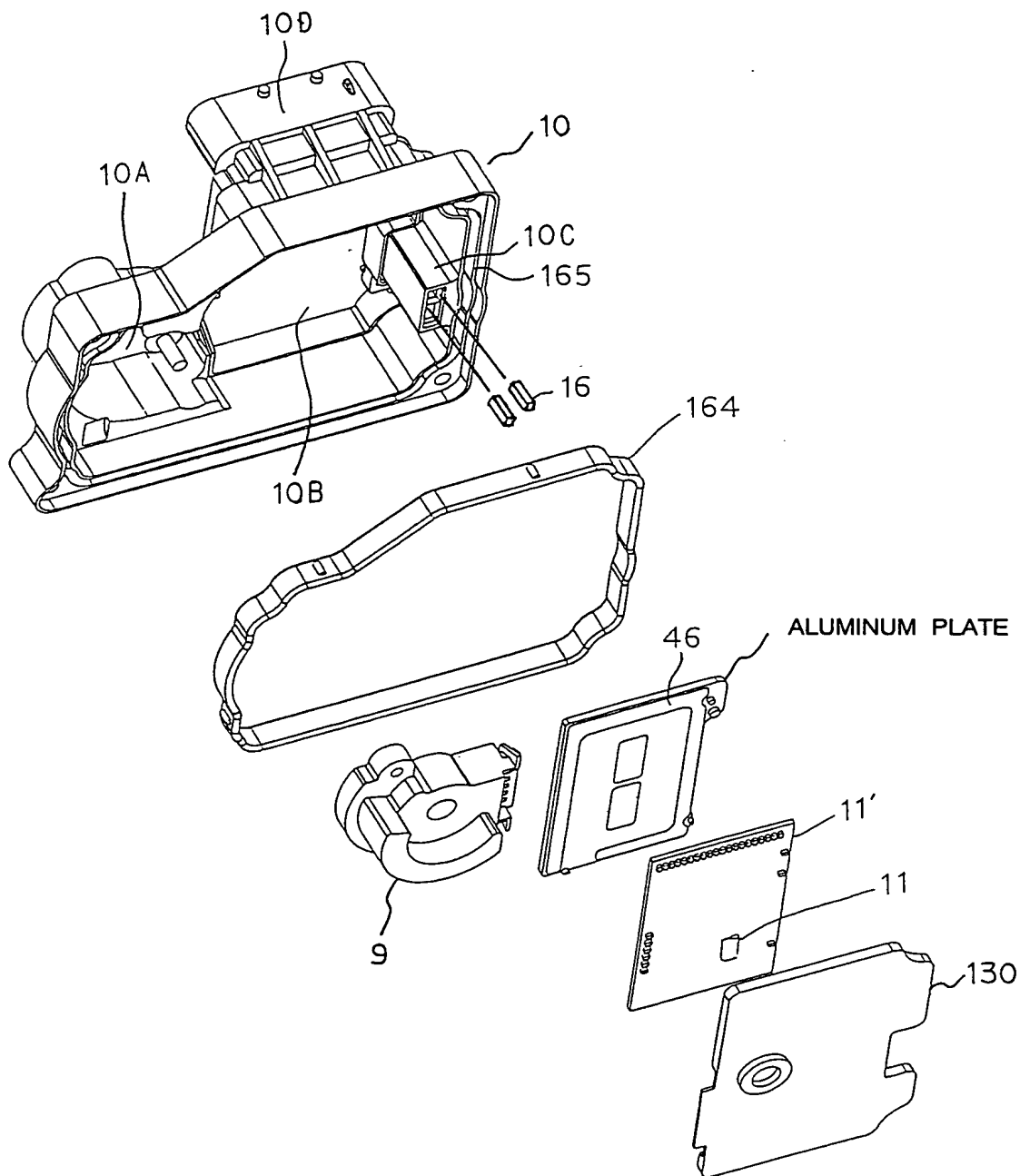
18/22

**FIG. 18**



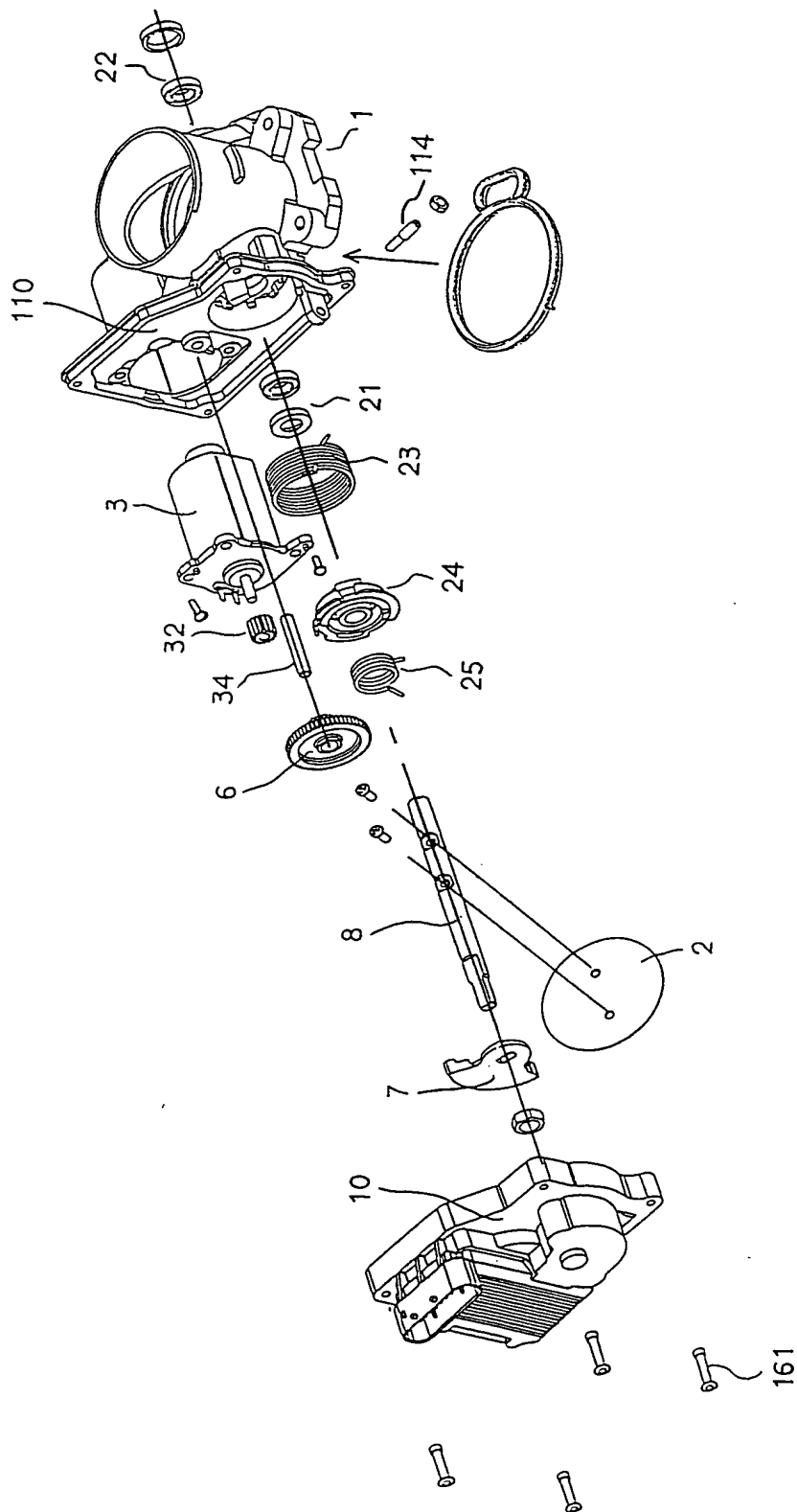
19/22

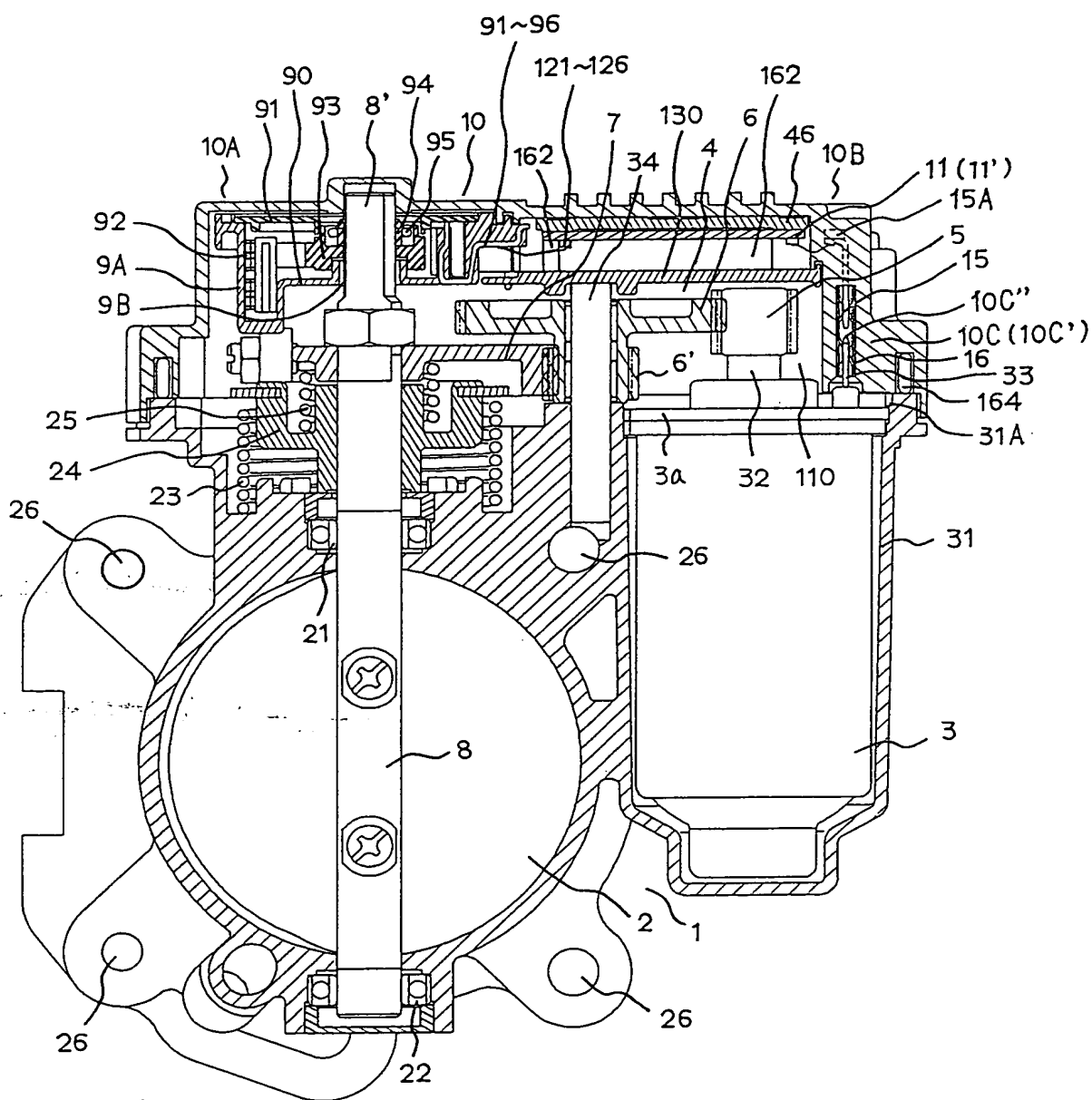
**FIG. 19**



20/22

**FIG. 20**





22/22

**FIG. 22**

